**文件编号：LTE-UE\_TEST\_(SYS)\_DYCS\_V1.0**

**CMW500系统消息(TDD)**

**测试方案说明书**

拟制：靳杰

时间：2012.6.6

**中国科学院计算技术研究所**

**无线通信技术研究中心**

**软件组**

**LTE协议栈研发项目组**

修改记录

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|  | 0.5 | 靳杰 | 2012-6-6 | 建立 |  |
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[1 引言 1](#_Toc326266164)

[1.1 编写目的 1](#_Toc326266165)

[1.2 背景 1](#_Toc326266166)

[1.3 定义 1](#_Toc326266167)

[1.4 参考资料 1](#_Toc326266168)

[2 概述 3](#_Toc326266169)

[3 测试方案 3](#_Toc326266170)

**测试设计方案说明书**

# 引言

## 编写目的

本说明书的读者为LTE UE端和Henb端协议栈系统联合调试人员、各模块的设计人员/编码人员/测试人员、项目组负责人、实验室主任及相关项目管理人员。

编写本说明书的目的在于：

* 为测试人员提供依据及方案。
* 为我们开发的TD-LTE协议栈提供一些重要的参数配置。
* 项目负责人将根据本文档计划和控制系统设计、开发的全过程。

按此做质量验证和软件评审。

## 背景

软件系统名称：TD- LTE HeNB协议栈软件系统

软件开发者：中科院计算所无线通信技术研究中心-LTE协议栈研发项目组

开发语言：C语言

硬件平台：暂无

软件平台：Linux

## 定义

RRC: Radio Resource Control

RLC: Radio Link Control

PDCP: Packet Data Convergence Protocol

MAC: Medium Access Control

RNTI: Radio Network Temporary Identifier

RB: Radio Bearer

LC(H): Logical Channel

## 参考资料

1. 《3GPP TS 36.300: "Evolved Universal Terrestrial Radio Access (E-UTRA) and Evolved Universal Terrestrial Radio Access (E-UTRAN); Overall description; Stage 2"》；
2. 《3GPP TS 36.321: "Evolved Universal Terrestrial Radio Access (E-UTRA); Medium Access Control (MAC) protocol specification"》；
3. 《3GPP TS 36.322:"Evolved Universal Terrestrial Radio Access (E-UTRA); Radio Link Control (RLC) protocol specification"》；
4. 《3GPP TS 36.323: "Evolved Universal Terrestrial Radio Access (E-UTRA); Packet Data Convergence Protocol (PDCP) Specification"》；
5. 《3GPP TS 36.331: "Evolved Universal Terrestrial Radio Access (E-UTRA); Radio Resource Control (RRC); Protocol specification"》；
6. 《LTE-HeNB协议栈软件系统\_核心数据管理模块概要设计说明书\_v1.0》。
7. 《LTE-HeNB协议栈软件系统\_RLC\_UM模块概要设计说明书\_v1.0》。
8. 《LTE-HeNB协议栈软件系统\_RLC发送模块概要设计说明书\_v1.0》。
9. 《LTE-HeNB协议栈软件系统\_RLC管理模块概要设计说明书\_v1.0》。
10. 《LTE-HeNB协议栈软件系统\_RLC接收模块概要设计说明书\_v1.0》。
11. 《LTE-HeNB协议栈软件系统PDCP子系统详细设计说明书\_v1.0》。
12. 《LTE-HeNB协议栈软件系统LowMAC控制模块详细设计说明书\_v1.0》。
13. 《LTE-HeNB协议栈软件系统MAC\_HARQ模块详细设计说明书\_v1.0 》。
14. 《LTE-HeNB协议栈软件系统MAC\_PDU构造模块详细设计说明书\_v1.0》。
15. 《LTE-HeNB协议栈软件系统MAC发送模块详细设计说明书\_v1.0》。
16. 《LTE-HeNB协议栈软件系统MAC接收模块详细设计说明书\_v1.0》。
17. 《LTE-HeNB协议栈软件系统MAC随机接入模块详细设计说明书\_v1.0》。
18. 《LTE-HeNB协议栈软件系统调度模块详细设计说明书\_v1.0》。

# 概述

根据CMW500提供的xml配置文件我们需要从中找出系统消息的一套配置参数，该文档将系统消息配置单独拿出来总结以便测试人员更快的进行查阅与参考。

# CMW500系统消息配置信息

***Type1：***

***1.*** ***Lte\_SystemInformationBlockType1\_Cell1.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType1" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<cellAccessRelatedInfo>

<plmn\_IdentityList COUNT="1"><!-- [1,6]-->

<INDEX>

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<INDEX VALUE="**0**"/><!--INTEGER [0,9]-->

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<INDEX VALUE="**1**"/><!--INTEGER [0,9]-->

</mcc>

<**mnc** COUNT="**2**"><!-- [2,3]-->

<INDEX VALUE="**0**"/><!--INTEGER [0,9]-->

<INDEX VALUE="**1**"/><!--INTEGER [0,9]-->

</mnc>

</plmn\_Identity>

<**cellReservedForOperatorUse** VALUE="**1**"/><!--ENUMERATED [0,1] reserved(0) **notReserved(1)**-->

</INDEX>

</plmn\_IdentityList>

<**trackingAreaCode** VALUE="**0000000000001101**"/><!--BITSTRING [16]-->

<**cellIdentity** VALUE="**0000000000111100000000000000**"/><!--BITSTRING [28]-->

<**cellBarred** VALUE="1"/><!--ENUMERATED [0,1] barred(0) **notBarred(1)**-->

<**intraFreqReselection** VALUE="0"/><!--ENUMERATED [0,1] **allowed(0)** notAllowed(1)-->

<**csg\_Indication** VALUE="**0**"/><!--BOOLEAN-->

</cellAccessRelatedInfo>

<cellSelectionInfo>

<**q\_RxLevMin** VALUE="**-66**"/><!--INTEGER [-70,-22]-->

<**q\_RxLevMinOffset** ACTIVE="1" VALUE="**1**"/><!--INTEGER [1,8]-->

</cellSelectionInfo>

<**freqBandIndicator** VALUE="**13**"/><!--INTEGER [1,64]-->

<**schedulingInfoList** COUNT="**3**"><!-- [1,maxSI\_Message(32)]-->

<INDEX>

<**si\_Periodicity** VALUE="1"/><!--ENUMERATED [0,6] rf8(0) **rf16(1)** rf32(2) rf64(3) rf128(4) rf256(5) rf512(6)-->

<**sib\_MappingInfo** COUNT="**1**"><!-- [0,maxSIB\_1(31)]-->

<INDEX VALUE="0"/><!--ENUMERATED [0,15] **sibType3(0)** sibType4(1) sibType5(2) sibType6(3) sibType7(4) sibType8(5) sibType9(6) sibType10(7) sibType11(8) sibType12\_v920(9) sibType13\_v920(10) spare5(11) spare4(12) spare3(13) spare2(14) spare1(15)-->

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</schedulingInfoList>

<**si\_WindowLength** VALUE="5"/><!--ENUMERATED [0,6] ms1(0) ms2(1) ms5(2) ms10(3) ms15(4) **ms20(5)** ms40(6)-->

<**systemInfoValueTag** VALUE="**0**"/><!--INTEGER [0,31]-->

</CONSTRAINT>

</MDDB>

***2. Lte\_SystemInformationBlockType1\_Tdd\_Cell1.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType1" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<cellAccessRelatedInfo>

<plmn\_IdentityList COUNT="1"><!-- [1,6]-->

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<**plmn\_Identity**>

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<INDEX VALUE="**0**"/><!--INTEGER [0,9]-->

<INDEX VALUE="**1**"/><!--INTEGER [0,9]-->

</mnc>

</plmn\_Identity>

<**cellReservedForOperatorUse** VALUE="1"/><!--ENUMERATED [0,1] reserved(0) **notReserved(1)**-->

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</plmn\_IdentityList>

<**trackingAreaCode** VALUE="**0000000000001101**"/><!--BITSTRING [16]-->

<**cellIdentity** VALUE="**0000000000111100000000000000**"/><!--BITSTRING [28]-->

<**cellBarred** VALUE="**1**"/><!--ENUMERATED [0,1] barred(0) **notBarred(1)**-->

<**intraFreqReselection** VALUE="0"/><!--ENUMERATED [0,1] **allowed(0)** notAllowed(1)-->

<**csg\_Indication** VALUE="**0**"/><!--BOOLEAN-->

</cellAccessRelatedInfo>

<cellSelectionInfo>

<**q\_RxLevMin** VALUE="**-66**"/><!--INTEGER [-70,-22]-->

<**q\_RxLevMinOffset** ACTIVE="1" VALUE="**1**"/><!--INTEGER [1,8]-->

</cellSelectionInfo>

<**freqBandIndicator** VALUE="**4**"/><!--INTEGER [1,64]-->

<**schedulingInfoList** COUNT="**3**"><!-- [1,maxSI\_Message(32)]-->

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<**si\_Periodicity** VALUE="**1**"/><!--ENUMERATED [0,6] rf8(0) **rf16(1)** rf32(2) rf64(3) rf128(4) rf256(5) rf512(6)-->

<**sib\_MappingInfo** COUNT="**1**"><!-- [0,maxSIB\_1(31)]-->

<INDEX VALUE="0"/><!--ENUMERATED [0,15] **sibType3(0)** sibType4(1) sibType5(2) sibType6(3) sibType7(4) sibType8(5) sibType9(6) sibType10(7) sibType11(8) sibType12\_v920(9) sibType13\_v920(10) spare5(11) spare4(12) spare3(13) spare2(14) spare1(15)-->

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<**sib\_MappingInfo** COUNT="**1**"><!-- [0,maxSIB\_1(31)]-->

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<**si\_Periodicity** VALUE="**3**"/><!--ENUMERATED [0,6] rf8(0) rf16(1) rf32(2) **rf64(3)** rf128(4) rf256(5) rf512(6)-->

<**sib\_MappingInfo** COUNT="**1**"><!-- [0,maxSIB\_1(31)]-->

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</sib\_MappingInfo>

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</schedulingInfoList>

<tdd\_Config ACTIVE="1">

<**subframeAssignment** VALUE="1"/><!--ENUMERATED [0,6] sa0(0) **sa1(1)** sa2(2) sa3(3) sa4(4) sa5(5) sa6(6)-->

<**specialSubframePatterns** VALUE="7"/><!--ENUMERATED [0,8] ssp0(0) ssp1(1) ssp2(2) ssp3(3) ssp4(4) ssp5(5) ssp6(6) **ssp7(7)** ssp8(8)-->

</tdd\_Config>

<**si\_WindowLength** VALUE="**5**"/><!--ENUMERATED [0,6] ms1(0) ms2(1) ms5(2) ms10(3) ms15(4) **ms20(5)** ms40(6)-->

<**systemInfoValueTag** VALUE="**0**"/><!--INTEGER [0,31]-->

</CONSTRAINT>

</MDDB>

***3. Lte\_SystemInformationBlockType1\_Tdd.xml（主要使用）***

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<CONSTRAINT NAME="\_SystemInformationBlockType1" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<cellAccessRelatedInfo>

<plmn\_IdentityList COUNT="1"><!-- [1,6]-->

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<**plmn\_Identity**>

<**mcc** ACTIVE="1" COUNT="**3**"><!-- [3]-->

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<INDEX VALUE="**0**"/><!--INTEGER [0,9]-->

<INDEX VALUE="**1**"/><!--INTEGER [0,9]-->

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<**mnc** COUNT="**2**"><!-- [2,3]-->

<INDEX VALUE="**0**"/><!--INTEGER [0,9]-->

<INDEX VALUE="**1**"/><!--INTEGER [0,9]-->

</mnc>

</plmn\_Identity>

<**cellReservedForOperatorUse** VALUE="1"/><!--ENUMERATED [0,1] reserved(0) **notReserved(1)**-->

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</plmn\_IdentityList>

<**trackingAreaCode** VALUE="**0000000000001011**"/><!--BITSTRING [16]-->

<**cellIdentity** VALUE="**0000000000111000000000000000**"/><!--BITSTRING [28]-->

<**cellBarred** VALUE="1"/><!--ENUMERATED [0,1] barred(0) **notBarred(1)**-->

<**intraFreqReselection** VALUE="0"/><!--ENUMERATED [0,1] **allowed(0)** notAllowed(1)-->

<**csg\_Indication** VALUE="**0**"/><!--BOOLEAN-->

</cellAccessRelatedInfo>

<cellSelectionInfo>

<**q\_RxLevMin** VALUE="**-66**"/><!--INTEGER [-70,-22]-->

<**q\_RxLevMinOffset** ACTIVE="**1**" VALUE="**1**"/><!--INTEGER [1,8]-->

</cellSelectionInfo>

<**freqBandIndicator** VALUE="**4**"/><!--INTEGER [1,64]-->

<**schedulingInfoList** COUNT="**3**"><!-- [1,maxSI\_Message(32)]-->

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<**si\_Periodicity** VALUE="**1**"/><!--ENUMERATED [0,6] rf8(0) **rf16(1)** rf32(2) rf64(3) rf128(4) rf256(5) rf512(6)-->

<**sib\_MappingInfo** COUNT="**1**"><!-- [0,maxSIB\_1(31)]-->

<INDEX VALUE="0"/><!--ENUMERATED [0,15] **sibType3(0)** sibType4(1) sibType5(2) sibType6(3) sibType7(4) sibType8(5) sibType9(6) sibType10(7) sibType11(8) sibType12\_v920(9) sibType13\_v920(10) spare5(11) spare4(12) spare3(13) spare2(14) spare1(15)-->

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<**si\_Periodicity** VALUE="**2**"/><!--ENUMERATED [0,6] rf8(0) rf16(1) **rf32(2)** rf64(3) rf128(4) rf256(5) rf512(6)-->

<**sib\_MappingInfo** COUNT="**1**"><!-- [0,maxSIB\_1(31)]-->

<INDEX VALUE="**1**"/><!--ENUMERATED [0,15] sibType3(0) **sibType4(1)** sibType5(2) sibType6(3) sibType7(4) sibType8(5) sibType9(6) sibType10(7) sibType11(8) sibType12\_v920(9) sibType13\_v920(10) spare5(11) spare4(12) spare3(13) spare2(14) spare1(15)-->

</sib\_MappingInfo>

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<INDEX>

<**si\_Periodicity** VALUE="**3**"/><!--ENUMERATED [0,6] rf8(0) rf16(1) rf32(2) **rf64(3)** rf128(4) rf256(5) rf512(6)-->

<sib\_MappingInfo COUNT="1"><!-- [0,maxSIB\_1(31)]-->

<INDEX VALUE="**2**"/><!--ENUMERATED [0,15] sibType3(0) sibType4(1) **sibType5(2)** sibType6(3) sibType7(4) sibType8(5) sibType9(6) sibType10(7) sibType11(8) sibType12\_v920(9) sibType13\_v920(10) spare5(11) spare4(12) spare3(13) spare2(14) spare1(15)-->

</sib\_MappingInfo>

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</schedulingInfoList>

<tdd\_Config ACTIVE="1">

<**subframeAssignment** VALUE="1"/><!--ENUMERATED [0,6] sa0(0) **sa1(1)** sa2(2) sa3(3) sa4(4) sa5(5) sa6(6)-->

<**specialSubframePatterns** VALUE="7"/><!--ENUMERATED [0,8] ssp0(0) ssp1(1) ssp2(2) ssp3(3) ssp4(4) ssp5(5) ssp6(6) **ssp7(7)** ssp8(8)-->

</tdd\_Config>

<**si\_WindowLength** VALUE="5"/><!--ENUMERATED [0,6] ms1(0) ms2(1) ms5(2) ms10(3) ms15(4) **ms20(5)** ms40(6)-->

<**systemInfoValueTag** VALUE="**0**"/><!--INTEGER [0,31]-->

</CONSTRAINT>

</MDDB>

***4. Lte\_SystemInformationBlockType1.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType1" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<cellAccessRelatedInfo>

<plmn\_IdentityList COUNT="1"><!-- [1,6]-->

<INDEX>

<**plmn\_Identity**>

<**mcc** ACTIVE="1" COUNT="**3**"><!-- [3]-->

<INDEX VALUE="**0**"/><!--INTEGER [0,9]-->

<INDEX VALUE="**0**"/><!--INTEGER [0,9]-->

<INDEX VALUE="**1**"/><!--INTEGER [0,9]-->

</mcc>

<**mnc** COUNT="**2**"><!-- [2,3]-->

<INDEX VALUE="**0**"/><!--INTEGER [0,9]-->

<INDEX VALUE="**1**"/><!--INTEGER [0,9]-->

</mnc>

</plmn\_Identity>

<**cellReservedForOperatorUse** VALUE="1"/><!--ENUMERATED [0,1] reserved(0) **notReserved(1)**-->

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</plmn\_IdentityList>

<**trackingAreaCode** VALUE="**0000000000001011**"/><!--BITSTRING [16]-->

<**cellIdentity** VALUE="**0000000000111000000000000000**"/><!--BITSTRING [28]-->

<**cellBarred** VALUE="1"/><!--ENUMERATED [0,1] barred(0) **notBarred(1)**-->

<**intraFreqReselection** VALUE="0"/><!--ENUMERATED [0,1] **allowed(0)** notAllowed(1)-->

<**csg\_Indication** VALUE="**0**"/><!--BOOLEAN-->

</cellAccessRelatedInfo>

<cellSelectionInfo>

<**q\_RxLevMin** VALUE="**-66**"/><!--INTEGER [-70,-22]-->

<**q\_RxLevMinOffset** ACTIVE="1" VALUE="**1**"/><!--INTEGER [1,8]-->

</cellSelectionInfo>

<**freqBandIndicator** VALUE="**4**"/><!--INTEGER [1,64]-->

<**schedulingInfoList** COUNT="**3**"><!-- [1,maxSI\_Message(32)]-->

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<**si\_Periodicity** VALUE="**3**"/><!--ENUMERATED [0,6] rf8(0) rf16(1) rf32(2) **rf64(3)** rf128(4) rf256(5) rf512(6)-->

<**sib\_MappingInfo** COUNT="1"><!-- [0,maxSIB\_1(31)]-->

<INDEX VALUE="2"/><!--ENUMERATED [0,15] sibType3(0) sibType4(1) **sibType5(2)** sibType6(3) sibType7(4) sibType8(5) sibType9(6) sibType10(7) sibType11(8) sibType12\_v920(9) sibType13\_v920(10) spare5(11) spare4(12) spare3(13) spare2(14) spare1(15)-->

</sib\_MappingInfo>

</INDEX>

</schedulingInfoList>

<**si\_WindowLength** VALUE="5"/><!--ENUMERATED [0,6] ms1(0) ms2(1) ms5(2) ms10(3) ms15(4) **ms20(5)** ms40(6)-->

<**systemInfoValueTag** VALUE="**0**"/><!--INTEGER [0,31]-->

</CONSTRAINT>

</MDDB>

***Type2：***

***1.*** ***Lte\_SystemInformationBlockType2\_Cell1.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType2" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<radioResourceConfigCommon>

<rach\_ConfigCommon>

<preambleInfo>

<**numberOfRA**\_**Preambles** VALUE="**15**"/><!--ENUMERATED [0,15] n4(0) n8(1) n12(2) n16(3) n20(4) n24(5) n28(6) n32(7) n36(8) n40(9) n44(10) n48(11) n52(12) n56(13) n60(14) **n64(15)**-->

</preambleInfo>

<powerRampingParameters>

<**powerRampingStep** VALUE="1"/><!--ENUMERATED [0,3] dB0(0) **dB2(1)** dB4(2) dB6(3)-->

<**preambleInitialReceivedTargetPower** VALUE="6"/><!--ENUMERATED [0,15] dBm\_120(0) dBm\_118(1) dBm\_116(2) dBm\_114(3) dBm\_112(4) dBm\_110(5) **dBm\_108(6)** dBm\_106(7) dBm\_104(8) dBm\_102(9) dBm\_100(10) dBm\_98(11) dBm\_96(12) dBm\_94(13) dBm\_92(14) dBm\_90(15)-->

</powerRampingParameters>

<ra\_SupervisionInfo>

<**preambleTransMax** VALUE="4"/><!--ENUMERATED [0,10] n3(0) n4(1) n5(2) n6(3) **n7(4)** n8(5) n10(6) n20(7) n50(8) n100(9) n200(10)-->

<**ra**\_**ResponseWindowSize** VALUE="7"/><!--ENUMERATED [0,7] sf2(0) sf3(1) sf4(2) sf5(3) sf6(4) sf7(5) sf8(6) **sf10(7)**-->

<**mac**\_**ContentionResolutionTimer** VALUE="7"/><!--ENUMERATED [0,7] sf8(0) sf16(1) sf24(2) sf32(3) sf40(4) sf48(5) sf56(6) **sf64(7)**-->

</ra\_SupervisionInfo>

<**maxHARQ**\_**Msg3Tx** VALUE="**4**"/><!--INTEGER [1,8]-->

</rach\_ConfigCommon>

<bcch\_Config>

<**modificationPeriodCoeff** VALUE="0"/><!--ENUMERATED [0,3] **n2(0)** n4(1) n8(2) n16(3)-->

</bcch\_Config>

<pcch\_Config>

<**defaultPagingCycle** VALUE="1"/><!--ENUMERATED [0,3] rf32(0) **rf64(1)** rf128(2) rf256(3)-->

<**nB** VALUE="3"/><!--ENUMERATED [0,7] fourT(0) twoT(1) oneT(2) **halfT(3)** quarterT(4) oneEighthT(5) oneSixteenthT(6) oneThirtySecondT(7)-->

</pcch\_Config>

<prach\_Config>

<**rootSequenceIndex** VALUE="**256**"/><!--INTEGER [0,837]-->

<prach\_ConfigInfo>

<**prach**\_**ConfigIndex** VALUE="**0**"/><!--INTEGER [0,63]-->

<**highSpeedFlag** VALUE="**0**"/><!--BOOLEAN-->

<**zeroCorrelationZoneConfig** VALUE="**9**"/><!--INTEGER [0,15]-->

<**prach**\_**FreqOffset** VALUE="**11**"/><!--INTEGER [0,94]-->

</prach\_ConfigInfo>

</prach\_Config>

<pdsch\_ConfigCommon>

<**referenceSignalPower** VALUE="**0**"/><!--INTEGER [-60,50]-->

<**p\_b** VALUE="**2**"/><!--INTEGER [0,3]-->

</pdsch\_ConfigCommon>

<pusch\_ConfigCommon>

<pusch\_ConfigBasic>

<**n\_SB** VALUE="**1**"/><!--INTEGER [1,4]-->

<**hoppingMode** VALUE="0"/><!--ENUMERATED [0,1] **interSubFrame(0)** intraAndInterSubFrame(1)-->

<**pusch**\_**HoppingOffset** VALUE="**0**"/><!--INTEGER [0,98]-->

<**enable64QAM** VALUE="**0**"/><!--BOOLEAN-->

</pusch\_ConfigBasic>

<ul\_ReferenceSignalsPUSCH>

<**groupHoppingEnabled** VALUE="**0**"/><!--BOOLEAN-->

<**groupAssignmentPUSCH** VALUE="**0**"/><!--INTEGER [0,29]-->

<**sequenceHoppingEnabled** VALUE="**0**"/><!--BOOLEAN-->

<**cyclicShift** VALUE="**0**"/><!--INTEGER [0,7]-->

</ul\_ReferenceSignalsPUSCH>

</pusch\_ConfigCommon>

<pucch\_ConfigCommon>

<**deltaPUCCH**\_**Shift** VALUE="0"/><!--ENUMERATED [0,2] **ds1(0)** ds2(1) ds3(2)-->

<**nRB**\_**CQI** VALUE="**0**"/><!--INTEGER [0,98]-->

<**nCS**\_**AN** VALUE="**2**"/><!--INTEGER [0,7]-->

<**n1PUCCH**\_**AN** VALUE="**0**"/><!--INTEGER [0,2047]-->

</pucch\_ConfigCommon>

<**soundingRS**\_**UL**\_**ConfigCommon** SELECT="1"><!--CHOICE release(0) **setup(1)**-->

<setup>

<**srs**\_**BandwidthConfig** VALUE="0"/><!--ENUMERATED [0,7] **bw0(0)** bw1(1) bw2(2) bw3(3) bw4(4) bw5(5) bw6(6) bw7(7)-->

<**srs**\_**SubframeConfig** VALUE="14"/><!--ENUMERATED [0,15] sc0(0) sc1(1) sc2(2) sc3(3) sc4(4) sc5(5) sc6(6) sc7(7) sc8(8) sc9(9) sc10(10) sc11(11) sc12(12) sc13(13) **sc14(14)** sc15(15)-->

<**ackNackSRS**\_**SimultaneousTransmission** VALUE="**0**"/><!--BOOLEAN-->

</setup>

</soundingRS\_UL\_ConfigCommon>

<uplinkPowerControlCommon>

<**p0**\_**NominalPUSCH** VALUE="**-99**"/><!--INTEGER [-126,24]-->

<**alpha** VALUE="**6**"/><!--ENUMERATED [0,7] al0(0) al04(1) al05(2) al06(3) al07(4) al08(5) **al09(6)** al1(7)-->

<**p0**\_**NominalPUCCH** VALUE="**-96**"/><!--INTEGER [-127,-96]-->

<deltaFList\_PUCCH>

<**deltaF**\_**PUCCH**\_**Format1** VALUE="0"/><!--ENUMERATED [0,2] **deltaF\_2(0)** deltaF0(1) deltaF2(2)-->

<**deltaF**\_**PUCCH**\_**Format1b** VALUE="0"/><!--ENUMERATED [0,2] **deltaF1(0)** deltaF3(1) deltaF5(2)-->

<**deltaF**\_**PUCCH**\_**Format2** VALUE="0"/><!--ENUMERATED [0,3] **deltaF\_2(0)** deltaF0(1) deltaF1(2) deltaF2(3)-->

<**deltaF**\_**PUCCH**\_**Format2a** VALUE="0"/><!--ENUMERATED [0,2] **deltaF\_2(0)** deltaF0(1) deltaF2(2)-->

<**deltaF**\_**PUCCH**\_**Format2b** VALUE="0"/><!--ENUMERATED [0,2] **deltaF\_2(0)** deltaF0(1) deltaF2(2)-->

</deltaFList\_PUCCH>

<**deltaPreambleMsg3** VALUE="**-1**"/><!--INTEGER [-1,6]-->

</uplinkPowerControlCommon>

<**ul**\_**CyclicPrefixLength** VALUE="0"/><!--ENUMERATED [0,1] **len1(0)** len2(1)-->

</radioResourceConfigCommon>

<ue\_TimersAndConstants>

<**t300** VALUE="3"/><!--ENUMERATED [0,7] ms100(0) ms200(1) ms300(2) **ms400(3)** ms600(4) ms1000(5) ms1500(6) ms2000(7)-->

<**t301** VALUE="3"/><!--ENUMERATED [0,7] ms100(0) ms200(1) ms300(2) **ms400(3)** ms600(4) ms1000(5) ms1500(6) ms2000(7)-->

<**t310** VALUE="2"/><!--ENUMERATED [0,6] ms0(0) ms50(1) **ms100(2)** ms200(3) ms500(4) ms1000(5) ms2000(6)-->

<**n310** VALUE="0"/><!--ENUMERATED [0,7] **n1(0)** n2(1) n3(2) n4(3) n6(4) n8(5) n10(6) n20(7)-->

<**t311** VALUE="2"/><!--ENUMERATED [0,6] ms1000(0) ms3000(1) **ms5000(2)** ms10000(3) ms15000(4) ms20000(5) ms30000(6)-->

<**n311** VALUE="0"/><!--ENUMERATED [0,7] **n1(0)** n2(1) n3(2) n4(3) n5(4) n6(5) n8(6) n10(7)-->

</ue\_TimersAndConstants>

<freqInfo>

<**ul**\_**CarrierFreq** ACTIVE="1" VALUE="**20800**"/><!--INTEGER [0,maxEARFCN(65535)]-->

<**ul**\_**Bandwidth** ACTIVE="1" VALUE="3"/><!--ENUMERATED [0,5] n6(0) n15(1) n25(2) **n50(3)** n75(4) n100(5)-->

<**additionalSpectrumEmission** VALUE="**1**"/><!--INTEGER [1,32]-->

</freqInfo>

<**timeAlignmentTimerCommon** VALUE="7"/><!--ENUMERATED [0,7] sf500(0) sf750(1) sf1280(2) sf1920(3) sf2560(4) sf5120(5) sf10240(6) **infinity(7)**-->

</CONSTRAINT>

</MDDB>

***2. Lte\_SystemInformationBlockType2\_Tdd\_Cell1.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType2" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<radioResourceConfigCommon>

<rach\_ConfigCommon>

<preambleInfo>

<**numberOfRA**\_**Preambles** VALUE="15"/><!--ENUMERATED [0,15] n4(0) n8(1) n12(2) n16(3) n20(4) n24(5) n28(6) n32(7) n36(8) n40(9) n44(10) n48(11) n52(12) n56(13) n60(14) **n64(15)**-->

</preambleInfo>

<powerRampingParameters>

<**powerRampingStep** VALUE="1"/><!--ENUMERATED [0,3] dB0(0) **dB2(1)** dB4(2) dB6(3)-->

<**preambleInitialReceivedTargetPower** VALUE="6"/><!--ENUMERATED [0,15] dBm\_120(0) dBm\_118(1) dBm\_116(2) dBm\_114(3) dBm\_112(4) dBm\_110(5) **dBm\_108(6)** dBm\_106(7) dBm\_104(8) dBm\_102(9) dBm\_100(10) dBm\_98(11) dBm\_96(12) dBm\_94(13) dBm\_92(14) dBm\_90(15)-->

</powerRampingParameters>

<ra\_SupervisionInfo>

<**preambleTransMax** VALUE="4"/><!--ENUMERATED [0,10] n3(0) n4(1) n5(2) n6(3) **n7(4)** n8(5) n10(6) n20(7) n50(8) n100(9) n200(10)-->

<**ra**\_**ResponseWindowSize** VALUE="7"/><!--ENUMERATED [0,7] sf2(0) sf3(1) sf4(2) sf5(3) sf6(4) sf7(5) sf8(6) **sf10(7)**-->

<**mac**\_**ContentionResolutionTimer** VALUE="7"/><!--ENUMERATED [0,7] sf8(0) sf16(1) sf24(2) sf32(3) sf40(4) sf48(5) sf56(6) **sf64(7)**-->

</ra\_SupervisionInfo>

<**maxHARQ**\_**Msg3Tx** VALUE="**4**"/><!--INTEGER [1,8]-->

</rach\_ConfigCommon>

<bcch\_Config>

<**modificationPeriodCoeff** VALUE="0"/><!--ENUMERATED [0,3] **n2(0)** n4(1) n8(2) n16(3)-->

</bcch\_Config>

<pcch\_Config>

<**defaultPagingCycle** VALUE="1"/><!--ENUMERATED [0,3] rf32(0) **rf64(1)** rf128(2) rf256(3)-->

<**nB** VALUE="3"/><!--ENUMERATED [0,7] fourT(0) twoT(1) oneT(2) **halfT(3)** quarterT(4) oneEighthT(5) oneSixteenthT(6) oneThirtySecondT(7)-->

</pcch\_Config>

<prach\_Config>

<**rootSequenceIndex** VALUE="**256**"/><!--INTEGER [0,837]-->

<prach\_ConfigInfo>

<**prach**\_**ConfigIndex** VALUE="**0**"/><!--INTEGER [0,63]-->

<**highSpeedFlag** VALUE="**0**"/><!--BOOLEAN-->

<**zeroCorrelationZoneConfig** VALUE="**9**"/><!--INTEGER [0,15]-->

<**prach**\_**FreqOffset** VALUE="**11**"/><!--INTEGER [0,94]-->

</prach\_ConfigInfo>

</prach\_Config>

<pdsch\_ConfigCommon>

<**referenceSignalPower** VALUE="**0**"/><!--INTEGER [-60,50]-->

<**p\_b** VALUE="**2**"/><!--INTEGER [0,3]-->

</pdsch\_ConfigCommon>

<pusch\_ConfigCommon>

<pusch\_ConfigBasic>

<**n\_SB** VALUE="**1**"/><!--INTEGER [1,4]-->

<**hoppingMode** VALUE="0"/><!--ENUMERATED [0,1] **interSubFrame(0)** intraAndInterSubFrame(1)-->

<**pusch**\_**HoppingOffset** VALUE="**0**"/><!--INTEGER [0,98]-->

<**enable64QAM** VALUE="**0**"/><!--BOOLEAN-->

</pusch\_ConfigBasic>

<ul\_ReferenceSignalsPUSCH>

<**groupHoppingEnabled** VALUE="**0**"/><!--BOOLEAN-->

<**groupAssignmentPUSCH** VALUE="**0**"/><!--INTEGER [0,29]-->

<**sequenceHoppingEnabled** VALUE="**0**"/><!--BOOLEAN-->

<**cyclicShift** VALUE="**0**"/><!--INTEGER [0,7]-->

</ul\_ReferenceSignalsPUSCH>

</pusch\_ConfigCommon>

<pucch\_ConfigCommon>

<**deltaPUCCH**\_**Shift** VALUE="0"/><!--ENUMERATED [0,2] **ds1(0)** ds2(1) ds3(2)-->

<**nRB**\_**CQI** VALUE="**0**"/><!--INTEGER [0,98]-->

<**nCS**\_**AN** VALUE="**2**"/><!--INTEGER [0,7]-->

<**n1PUCCH**\_**AN** **VALUE**="**0**"/><!--INTEGER [0,2047]-->

</pucch\_ConfigCommon>

<**soundingRS**\_**UL**\_**ConfigCommon** SELECT="1"><!--CHOICE release(0) **setup(1)**-->

<setup>

<**srs**\_**BandwidthConfig** VALUE="0"/><!--ENUMERATED [0,7] **bw0(0)** bw1(1) bw2(2) bw3(3) bw4(4) bw5(5) bw6(6) bw7(7)-->

<**srs**\_**SubframeConfig** VALUE="9"/><!--ENUMERATED [0,15] sc0(0) sc1(1) sc2(2) sc3(3) sc4(4) sc5(5) sc6(6) sc7(7) sc8(8) **sc9(9)** sc10(10) sc11(11) sc12(12) sc13(13) sc14(14) sc15(15)-->

<**ackNackSRS**\_**SimultaneousTransmission** VALUE="**0**"/><!--BOOLEAN-->

</setup>

</soundingRS\_UL\_ConfigCommon>

<uplinkPowerControlCommon>

<**p0**\_**NominalPUSCH** VALUE="**-99**"/><!--INTEGER [-126,24]-->

<**alpha** VALUE="**6**"/><!--ENUMERATED [0,7] al0(0) al04(1) al05(2) al06(3) al07(4) al08(5) **al09(6)** al1(7)-->

<**p0**\_**NominalPUCCH** VALUE="**-96**"/><!--INTEGER [-127,-96]-->

<deltaFList\_PUCCH>

<**deltaF**\_**PUCCH**\_**Format1** VALUE="0"/><!--ENUMERATED [0,2] **deltaF\_2(0)** deltaF0(1) deltaF2(2)-->

<**deltaF**\_**PUCCH**\_**Format1b** VALUE="0"/><!--ENUMERATED [0,2] **deltaF1(0)** deltaF3(1) deltaF5(2)-->

<**deltaF**\_**PUCCH**\_**Format2** VALUE="0"/><!--ENUMERATED [0,3] **deltaF\_2(0)** deltaF0(1) deltaF1(2) deltaF2(3)-->

<**deltaF**\_**PUCCH**\_**Format2a** VALUE="0"/><!--ENUMERATED [0,2] **deltaF\_2(0)** deltaF0(1) deltaF2(2)-->

<**deltaF**\_**PUCCH**\_**Format2b** VALUE="0"/><!--ENUMERATED [0,2] **deltaF\_2(0)** deltaF0(1) deltaF2(2)-->

</deltaFList\_PUCCH>

<**deltaPreambleMsg3** VALUE="**-1**"/><!--INTEGER [-1,6]-->

</uplinkPowerControlCommon>

<**ul**\_**CyclicPrefixLength** VALUE="0"/><!--ENUMERATED [0,1] **len1(0)** len2(1)-->

</radioResourceConfigCommon>

<ue\_TimersAndConstants>

<**t300** VALUE="3"/><!--ENUMERATED [0,7] ms100(0) ms200(1) ms300(2) **ms400(3)** ms600(4) ms1000(5) ms1500(6) ms2000(7)-->

<**t301** VALUE="3"/><!--ENUMERATED [0,7] ms100(0) ms200(1) ms300(2) **ms400(3)** ms600(4) ms1000(5) ms1500(6) ms2000(7)-->

<**t310** VALUE="2"/><!--ENUMERATED [0,6] ms0(0) ms50(1) **ms100(2)** ms200(3) ms500(4) ms1000(5) ms2000(6)-->

<**n310** VALUE="0"/><!--ENUMERATED [0,7] **n1(0)** n2(1) n3(2) n4(3) n6(4) n8(5) n10(6) n20(7)-->

<**t311** VALUE="2"/><!--ENUMERATED [0,6] ms1000(0) ms3000(1) **ms5000(2)** ms10000(3) ms15000(4) ms20000(5) ms30000(6)-->

<**n311** VALUE="0"/><!--ENUMERATED [0,7] **n1(0)** n2(1) n3(2) n4(3) n5(4) n6(5) n8(6) n10(7)-->

</ue\_TimersAndConstants>

<freqInfo>

<**ul**\_**CarrierFreq** ACTIVE="1" VALUE="**20800**"/><!--INTEGER [0,maxEARFCN(65535)]-->

<**ul**\_**Bandwidth** ACTIVE="1" VALUE="3"/><!--ENUMERATED [0,5] n6(0) n15(1) n25(2) **n50(3)** n75(4) n100(5)-->

<**additionalSpectrumEmission** VALUE="**1**"/><!--INTEGER [1,32]-->

</freqInfo>

<**timeAlignmentTimerCommon** VALUE="7"/><!--ENUMERATED [0,7] sf500(0) sf750(1) sf1280(2) sf1920(3) sf2560(4) sf5120(5) sf10240(6) **infinity(7)**-->

</CONSTRAINT>

</MDDB>

***3. Lte\_SystemInformationBlockType2\_Tdd.xml（主要使用）***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType2" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<radioResourceConfigCommon>

<rach\_ConfigCommon>

<preambleInfo>

<**numberOfRA**\_**Preambles** VALUE="15"/><!--ENUMERATED [0,15] n4(0) n8(1) n12(2) n16(3) n20(4) n24(5) n28(6) n32(7) n36(8) n40(9) n44(10) n48(11) n52(12) n56(13) n60(14) **n64(15)**-->

</preambleInfo>

<powerRampingParameters>

<**powerRampingStep** VALUE="1"/><!--ENUMERATED [0,3] dB0(0) **dB2(1)** dB4(2) dB6(3)-->

<**preambleInitialReceivedTargetPower** VALUE="6"/><!--ENUMERATED [0,15] dBm\_120(0) dBm\_118(1) dBm\_116(2) dBm\_114(3) dBm\_112(4) dBm\_110(5) **dBm\_108(6)** dBm\_106(7) dBm\_104(8) dBm\_102(9) dBm\_100(10) dBm\_98(11) dBm\_96(12) dBm\_94(13) dBm\_92(14) dBm\_90(15)-->

</powerRampingParameters>

<ra\_SupervisionInfo>

<**preambleTransMax** VALUE="4"/><!--ENUMERATED [0,10] n3(0) n4(1) n5(2) n6(3) **n7(4)** n8(5) n10(6) n20(7) n50(8) n100(9) n200(10)-->

<**ra**\_**ResponseWindowSize** VALUE="7"/><!--ENUMERATED [0,7] sf2(0) sf3(1) sf4(2) sf5(3) sf6(4) sf7(5) sf8(6) **sf10(7)**-->

<**mac**\_**ContentionResolutionTimer** VALUE="7"/><!--ENUMERATED [0,7] sf8(0) sf16(1) sf24(2) sf32(3) sf40(4) sf48(5) sf56(6) **sf64(7)**-->

</ra\_SupervisionInfo>

<**maxHARQ**\_**Msg3Tx** VALUE="**4**"/><!--INTEGER [1,8]-->

</rach\_ConfigCommon>

<bcch\_Config>

<**modificationPeriodCoeff** VALUE="0"/><!--ENUMERATED [0,3] **n2(0)** n4(1) n8(2) n16(3)-->

</bcch\_Config>

<pcch\_Config>

<**defaultPagingCycle** VALUE="1"/><!--ENUMERATED [0,3] rf32(0) **rf64(1)** rf128(2) rf256(3)-->

<**nB** VALUE="3"/><!--ENUMERATED [0,7] fourT(0) twoT(1) oneT(2) **halfT(3)** quarterT(4) oneEighthT(5) oneSixteenthT(6) oneThirtySecondT(7)-->

</pcch\_Config>

<prach\_Config>

<**rootSequenceIndex** VALUE="**123**"/><!--INTEGER [0,837]-->

<prach\_ConfigInfo>

<**prach**\_**ConfigIndex** VALUE="**0**"/><!--INTEGER [0,63]-->

<**highSpeedFlag** VALUE="**0**"/><!--BOOLEAN-->

<**zeroCorrelationZoneConfig** VALUE="**9**"/><!--INTEGER [0,15]-->

<**prach**\_**FreqOffset** VALUE="**5**"/><!--INTEGER [0,94]-->

</prach\_ConfigInfo>

</prach\_Config>

<pdsch\_ConfigCommon>

<**referenceSignalPower** VALUE="**0**"/><!--INTEGER [-60,50]-->

<**p\_b** VALUE="**2**"/><!--INTEGER [0,3]-->

</pdsch\_ConfigCommon>

<pusch\_ConfigCommon>

<pusch\_ConfigBasic>

<**n\_SB** VALUE="**1**"/><!--INTEGER [1,4]-->

<**hoppingMode** VALUE="0"/><!--ENUMERATED [0,1] **interSubFrame(0)** intraAndInterSubFrame(1)-->

<**pusch**\_**HoppingOffset** VALUE="**0**"/><!--INTEGER [0,98]-->

<**enable64QAM** VALUE="**0**"/><!--BOOLEAN-->

</pusch\_ConfigBasic>

<ul\_ReferenceSignalsPUSCH>

<**groupHoppingEnabled** VALUE="**0**"/><!--BOOLEAN-->

<**groupAssignmentPUSCH** VALUE="**0**"/><!--INTEGER [0,29]-->

<**sequenceHoppingEnabled** VALUE="**0**"/><!--BOOLEAN-->

<**cyclicShift** VALUE="**0**"/><!--INTEGER [0,7]-->

</ul\_ReferenceSignalsPUSCH>

</pusch\_ConfigCommon>

<pucch\_ConfigCommon>

<**deltaPUCCH**\_**Shift** VALUE="0"/><!--ENUMERATED [0,2] **ds1(0)** ds2(1) ds3(2)-->

<**nRB**\_**CQI** VALUE="**0**"/><!--INTEGER [0,98]-->

<**nCS**\_**AN** VALUE="**2**"/><!--INTEGER [0,7]-->

<**n1PUCCH**\_**AN** VALUE="**0**"/><!--INTEGER [0,2047]-->

</pucch\_ConfigCommon>

<**soundingRS**\_**UL**\_**ConfigCommon** SELECT="1"><!--CHOICE release(0) **setup(1)**-->

<setup>

<**srs**\_**BandwidthConfig** VALUE="0"/><!--ENUMERATED [0,7] **bw0(0)** bw1(1) bw2(2) bw3(3) bw4(4) bw5(5) bw6(6) bw7(7)-->

<**srs**\_**SubframeConfig** VALUE="9"/><!--ENUMERATED [0,15] sc0(0) sc1(1) sc2(2) sc3(3) sc4(4) sc5(5) sc6(6) sc7(7) sc8(8) **sc9(9)** sc10(10) sc11(11) sc12(12) sc13(13) sc14(14) sc15(15)-->

<**ackNackSRS**\_**SimultaneousTransmission** VALUE="**0**"/><!--BOOLEAN-->

</setup>

</soundingRS\_UL\_ConfigCommon>

<uplinkPowerControlCommon>

<**p0**\_**NominalPUSCH** VALUE="**-99**"/><!--INTEGER [-126,24]-->

<**alpha** VALUE="6"/><!--ENUMERATED [0,7] al0(0) al04(1) al05(2) al06(3) al07(4) al08(5) **al09(6)** al1(7)-->

<**p0**\_**NominalPUCCH** VALUE="**-96**"/><!--INTEGER [-127,-96]-->

<deltaFList\_PUCCH>

<**deltaF**\_**PUCCH**\_**Format1** VALUE="0"/><!--ENUMERATED [0,2] **deltaF\_2(0)** deltaF0(1) deltaF2(2)-->

<**deltaF**\_**PUCCH**\_**Format1b** VALUE="0"/><!--ENUMERATED [0,2] **deltaF1(0)** deltaF3(1) deltaF5(2)-->

<**deltaF**\_**PUCCH**\_**Format2** VALUE="0"/><!--ENUMERATED [0,3] **deltaF\_2(0)** deltaF0(1) deltaF1(2) deltaF2(3)-->

<**deltaF**\_**PUCCH**\_**Format2a** VALUE="0"/><!--ENUMERATED [0,2] **deltaF\_2(0)** deltaF0(1) deltaF2(2)-->

<**deltaF**\_**PUCCH**\_**Format2b** VALUE="0"/><!--ENUMERATED [0,2] **deltaF\_2(0)** deltaF0(1) deltaF2(2)-->

</deltaFList\_PUCCH>

<**deltaPreambleMsg3** VALUE="**-1**"/><!--INTEGER [-1,6]-->

</uplinkPowerControlCommon>

<**ul**\_**CyclicPrefixLength** VALUE="0"/><!--ENUMERATED [0,1] **len1(0)** len2(1)-->

</radioResourceConfigCommon>

<ue\_TimersAndConstants>

<**t300** VALUE="3"/><!--ENUMERATED [0,7] ms100(0) ms200(1) ms300(2) **ms400(3)** ms600(4) ms1000(5) ms1500(6) ms2000(7)-->

<**t301** VALUE="3"/><!--ENUMERATED [0,7] ms100(0) ms200(1) ms300(2) **ms400(3)** ms600(4) ms1000(5) ms1500(6) ms2000(7)-->

<**t310** VALUE="2"/><!--ENUMERATED [0,6] ms0(0) ms50(1) **ms100(2)** ms200(3) ms500(4) ms1000(5) ms2000(6)-->

<**n310** VALUE="0"/><!--ENUMERATED [0,7] **n1(0)** n2(1) n3(2) n4(3) n6(4) n8(5) n10(6) n20(7)-->

<**t311** VALUE="2"/><!--ENUMERATED [0,6] ms1000(0) ms3000(1) **ms5000(2)** ms10000(3) ms15000(4) ms20000(5) ms30000(6)-->

<**n311** VALUE="0"/><!--ENUMERATED [0,7] **n1(0)** n2(1) n3(2) n4(3) n5(4) n6(5) n8(6) n10(7)-->

</ue\_TimersAndConstants>

<freqInfo>

<**ul**\_**CarrierFreq** ACTIVE="1" VALUE="**20800**"/><!--INTEGER [0,maxEARFCN(65535)]-->

<**ul**\_**Bandwidth** ACTIVE="1" VALUE="3"/><!--ENUMERATED [0,5] n6(0) n15(1) n25(2) **n50(3)** n75(4) n100(5)-->

<**additionalSpectrumEmission** VALUE="**1**"/><!--INTEGER [1,32]-->

</freqInfo>

<**timeAlignmentTimerCommon** VALUE="7"/><!--ENUMERATED [0,7] sf500(0) sf750(1) sf1280(2) sf1920(3) sf2560(4) sf5120(5) sf10240(6) **infinity(7)**-->

</CONSTRAINT>

</MDDB>

***4. Lte\_SystemInformationBlockType2.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType2" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<radioResourceConfigCommon>

<rach\_ConfigCommon>

<preambleInfo>

<**numberOfRA**\_**Preambles** VALUE="15"/><!--ENUMERATED [0,15] n4(0) n8(1) n12(2) n16(3) n20(4) n24(5) n28(6) n32(7) n36(8) n40(9) n44(10) n48(11) n52(12) n56(13) n60(14) **n64(15)**-->

</preambleInfo>

<powerRampingParameters>

<**powerRampingStep** VALUE="1"/><!--ENUMERATED [0,3] dB0(0) **dB2(1)** dB4(2) dB6(3)-->

<**preambleInitialReceivedTargetPower** VALUE="6"/><!--ENUMERATED [0,15] dBm\_120(0) dBm\_118(1) dBm\_116(2) dBm\_114(3) dBm\_112(4) dBm\_110(5) **dBm\_108(6)** dBm\_106(7) dBm\_104(8) dBm\_102(9) dBm\_100(10) dBm\_98(11) dBm\_96(12) dBm\_94(13) dBm\_92(14) dBm\_90(15)-->

</powerRampingParameters>

<ra\_SupervisionInfo>

<**preambleTransMax** VALUE="4"/><!--ENUMERATED [0,10] n3(0) n4(1) n5(2) n6(3) **n7(4)** n8(5) n10(6) n20(7) n50(8) n100(9) n200(10)-->

<**ra**\_**ResponseWindowSize** VALUE="7"/><!--ENUMERATED [0,7] sf2(0) sf3(1) sf4(2) sf5(3) sf6(4) sf7(5) sf8(6) **sf10(7)**-->

<**mac**\_**ContentionResolutionTimer** VALUE="7"/><!--ENUMERATED [0,7] sf8(0) sf16(1) sf24(2) sf32(3) sf40(4) sf48(5) sf56(6) **sf64(7)**-->

</ra\_SupervisionInfo>

<**maxHARQ**\_**Msg3Tx** VALUE="**4**"/><!--INTEGER [1,8]-->

</rach\_ConfigCommon>

<bcch\_Config>

<**modificationPeriodCoeff** VALUE="0"/><!--ENUMERATED [0,3] **n2(0)** n4(1) n8(2) n16(3)-->

</bcch\_Config>

<pcch\_Config>

<**defaultPagingCycle** VALUE="1"/><!--ENUMERATED [0,3] rf32(0) **rf64(1)** rf128(2) rf256(3)-->

<**nB** VALUE="3"/><!--ENUMERATED [0,7] fourT(0) twoT(1) oneT(2) **halfT(3)** quarterT(4) oneEighthT(5) oneSixteenthT(6) oneThirtySecondT(7)-->

</pcch\_Config>

<prach\_Config>

<**rootSequenceIndex** VALUE="**123**"/><!--INTEGER [0,837]-->

<prach\_ConfigInfo>

<**prach**\_**ConfigIndex** VALUE="**0**"/><!--INTEGER [0,63]-->

<**highSpeedFlag** VALUE="**0**"/><!--BOOLEAN-->

<**zeroCorrelationZoneConfig** VALUE="**9**"/><!--INTEGER [0,15]-->

<**prach**\_**FreqOffset** VALUE="**5**"/><!--INTEGER [0,94]-->

</prach\_ConfigInfo>

</prach\_Config>

<pdsch\_ConfigCommon>

<**referenceSignalPower** VALUE="**0**"/><!--INTEGER [-60,50]-->

<**p\_b** VALUE="**2**"/><!--INTEGER [0,3]-->

</pdsch\_ConfigCommon>

<pusch\_ConfigCommon>

<pusch\_ConfigBasic>

<**n\_SB** VALUE="**1**"/><!--INTEGER [1,4]-->

<**hoppingMode** VALUE="0"/><!--ENUMERATED [0,1] **interSubFrame(0)** intraAndInterSubFrame(1)-->

<**pusch**\_**HoppingOffset** VALUE="**0**"/><!--INTEGER [0,98]-->

<**enable64QAM** VALUE="**0**"/><!--BOOLEAN-->

</pusch\_ConfigBasic>

<ul\_ReferenceSignalsPUSCH>

<**groupHoppingEnabled** VALUE="**0**"/><!--BOOLEAN-->

<**groupAssignmentPUSCH** VALUE="**0**"/><!--INTEGER [0,29]-->

<**sequenceHoppingEnabled** VALUE="**0**"/><!--BOOLEAN-->

<**cyclicShift** VALUE="**0**"/><!--INTEGER [0,7]-->

</ul\_ReferenceSignalsPUSCH>

</pusch\_ConfigCommon>

<pucch\_ConfigCommon>

<**deltaPUCCH**\_**Shift** VALUE="0"/><!--ENUMERATED [0,2] **ds1(0)** ds2(1) ds3(2)-->

<**nRB\_CQI** VALUE="**0**"/><!--INTEGER [0,98]-->

<**nCS\_AN** VALUE="**2**"/><!--INTEGER [0,7]-->

<**n1PUCCH**\_**AN** VALUE="**0**"/><!--INTEGER [0,2047]-->

</pucch\_ConfigCommon>

<**soundingRS**\_**UL**\_**ConfigCommon** SELECT="1"><!--CHOICE release(0) **setup(1)**-->

<setup>

<**srs**\_**BandwidthConfig** VALUE="0"/><!--ENUMERATED [0,7] **bw0(0)** bw1(1) bw2(2) bw3(3) bw4(4) bw5(5) bw6(6) bw7(7)-->

<**srs**\_**SubframeConfig** VALUE="14"/><!--ENUMERATED [0,15] sc0(0) sc1(1) sc2(2) sc3(3) sc4(4) sc5(5) sc6(6) sc7(7) sc8(8) sc9(9) sc10(10) sc11(11) sc12(12) sc13(13) **sc14(14)** sc15(15)-->

<**ackNackSRS**\_**SimultaneousTransmission** VALUE="**0**"/><!--BOOLEAN-->

</setup>

</soundingRS\_UL\_ConfigCommon>

<uplinkPowerControlCommon>

<**p0**\_**NominalPUSCH** VALUE="**-99**"/><!--INTEGER [-126,24]-->

<**alpha** VALUE="6"/><!--ENUMERATED [0,7] al0(0) al04(1) al05(2) al06(3) al07(4) al08(5) **al09(6)** al1(7)-->

<**p0**\_**NominalPUCCH** VALUE="**-96**"/><!--INTEGER [-127,-96]-->

<deltaFList\_PUCCH>

<**deltaF**\_**PUCCH**\_**Format1** VALUE="0"/><!--ENUMERATED [0,2] **deltaF\_2(0)** deltaF0(1) deltaF2(2)-->

<**deltaF**\_**PUCCH**\_**Format1b** VALUE="0"/><!--ENUMERATED [0,2] **deltaF1(0)** deltaF3(1) deltaF5(2)-->

<**deltaF**\_**PUCCH**\_**Format2** VALUE="0"/><!--ENUMERATED [0,3] **deltaF\_2(0)** deltaF0(1) deltaF1(2) deltaF2(3)-->

<**deltaF**\_**PUCCH**\_**Format2a** VALUE="0"/><!--ENUMERATED [0,2] **deltaF\_2(0)** deltaF0(1) deltaF2(2)-->

<**deltaF**\_**PUCCH**\_**Format2b** VALUE="0"/><!--ENUMERATED [0,2] **deltaF\_2(0)** deltaF0(1) deltaF2(2)-->

</deltaFList\_PUCCH>

<**deltaPreambleMsg3** VALUE="**-1**"/><!--INTEGER [-1,6]-->

</uplinkPowerControlCommon>

<**ul**\_**CyclicPrefixLength** VALUE="0"/><!--ENUMERATED [0,1] **len1(0)** len2(1)-->

</radioResourceConfigCommon>

<ue\_TimersAndConstants>

<**t300** VALUE="3"/><!--ENUMERATED [0,7] ms100(0) ms200(1) ms300(2) **ms400(3)** ms600(4) ms1000(5) ms1500(6) ms2000(7)-->

<**t301** VALUE="3"/><!--ENUMERATED [0,7] ms100(0) ms200(1) ms300(2) **ms400(3)** ms600(4) ms1000(5) ms1500(6) ms2000(7)-->

<**t310** VALUE="2"/><!--ENUMERATED [0,6] ms0(0) ms50(1) **ms100(2)** ms200(3) ms500(4) ms1000(5) ms2000(6)-->

<**n310** VALUE="0"/><!--ENUMERATED [0,7] **n1(0)** n2(1) n3(2) n4(3) n6(4) n8(5) n10(6) n20(7)-->

<**t311** VALUE="2"/><!--ENUMERATED [0,6] ms1000(0) ms3000(1) **ms5000(2)** ms10000(3) ms15000(4) ms20000(5) ms30000(6)-->

<**n311** VALUE="0"/><!--ENUMERATED [0,7] **n1(0)** n2(1) n3(2) n4(3) n5(4) n6(5) n8(6) n10(7)-->

</ue\_TimersAndConstants>

<freqInfo>

<**ul**\_**CarrierFreq** ACTIVE="1" VALUE="**20800**"/><!--INTEGER [0,maxEARFCN(65535)]-->

<**ul**\_**Bandwidth** ACTIVE="1" VALUE="3"/><!--ENUMERATED [0,5] n6(0) n15(1) n25(2) **n50(3)** n75(4) n100(5)-->

<**additionalSpectrumEmission** VALUE="**1**"/><!--INTEGER [1,32]-->

</freqInfo>

<**timeAlignmentTimerCommon** VALUE="7"/><!--ENUMERATED [0,7] sf500(0) sf750(1) sf1280(2) sf1920(3) sf2560(4) sf5120(5) sf10240(6) **infinity(7)**-->

</CONSTRAINT>

</MDDB>

***Type3：***

***1.*** ***Lte\_SystemInformationBlockType3.xml（主要使用）***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType3" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<cellReselectionInfoCommon>

<**q\_Hyst** VALUE="7"/><!--ENUMERATED [0,15] dB0(0) dB1(1) dB2(2) dB3(3) dB4(4) dB5(5) dB6(6) **dB8(7)** dB10(8) dB12(9) dB14(10) dB16(11) dB18(12) dB20(13) dB22(14) dB24(15)-->

</cellReselectionInfoCommon>

<cellReselectionServingFreqInfo>

<**threshServingLow** VALUE="**9**"/><!--INTEGER [0,31]-->

<**cellReselectionPriority** VALUE="**0**"/><!--INTEGER [0,7]-->

</cellReselectionServingFreqInfo>

<intraFreqCellReselectionInfo>

<**q\_RxLevMin** VALUE="**-70**"/><!--INTEGER [-70,-22]-->

<**presenceAntennaPort1** VALUE="**0**"/><!--BOOLEAN-->

<**neighCellConfig** VALUE="**00**"/><!--BITSTRING [2]-->

<**t\_ReselectionEUTRA** VALUE="**0**"/><!--INTEGER [0,7]-->

</intraFreqCellReselectionInfo>

</CONSTRAINT>

</MDDB>

***Type4：***

***1.*** ***Lte\_SystemInformationBlockType4.xml（主要使用）***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType4" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

</CONSTRAINT>

</MDDB>

***Type5：***

***1.*** ***Lte\_SystemInformationBlockType5.xml（主要使用）***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType5" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**interFreqCarrierFreqList** COUNT="**1**"><!-- [1,maxFreq(8)]-->

<INDEX>

<**dl**\_**CarrierFreq** VALUE="**2350**"/><!--INTEGER [0,maxEARFCN(65535)]-->

<**q\_RxLevMin** VALUE="**-70**"/><!--INTEGER [-70,-22]-->

<**t\_ReselectionEUTRA** VALUE="**1**"/><!--INTEGER [0,7]-->

<**threshX**\_**High** VALUE="**5**"/><!--INTEGER [0,31]-->

<**threshX**\_**Low** VALUE="**5**"/><!--INTEGER [0,31]-->

<**allowedMeasBandwidth** VALUE="**3**"/><!--ENUMERATED [0,5] mbw6(0) mbw15(1) mbw25(2) **mbw50(3)** mbw75(4) mbw100(5)-->

<**presenceAntennaPort1** VALUE="**0**"/><!--BOOLEAN-->

<**neighCellConfig** VALUE="**00**"/><!--BITSTRING [2]-->

</INDEX>

</interFreqCarrierFreqList>

</CONSTRAINT>

</MDDB>

***Type6：***

***1.*** ***Lte\_SystemInformationBlockType6.xml（主要使用）***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType6" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**carrierFreqListUTRA**\_**FDD** ACTIVE="**1**" COUNT="**1**"><!-- [1,maxUTRA\_FDD\_Carrier(16)]-->

<INDEX>

<**carrierFreq** VALUE="**0**"/><!--INTEGER [0,16383]-->

<**threshX**\_**High** VALUE="**0**"/><!--INTEGER [0,31]-->

<**threshX**\_**Low** VALUE="**0**"/><!--INTEGER [0,31]-->

<**q\_RxLevMin** VALUE="**-60**"/><!--INTEGER [-60,-13]-->

<**p\_MaxUTRA** VALUE="**-50**"/><!--INTEGER [-50,33]-->

<**q\_QualMin** VALUE="**-24**"/><!--INTEGER [-24,0]-->

</INDEX>

</carrierFreqListUTRA\_FDD>

**<t\_ReselectionUTRA** VALUE="**0**"/><!--INTEGER [0,7]-->

</CONSTRAINT>

</MDDB>

***Type7：***

***1.*** ***Lte\_SystemInformationBlockType7.xml（主要使用）***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType7" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**t\_ReselectionGERAN** VALUE="**0**"/><!--INTEGER [0,7]-->

<**t\_ReselectionGERAN**\_SF ACTIVE="**1**">

<**sf**\_**Medium** VALUE="0"/><!--ENUMERATED [0,3] **oDot25(0)** oDot5(1) oDot75(2) lDot0(3)-->

<**sf\_High** VALUE="**0**"/><!--ENUMERATED [0,3] **oDot25(0)** oDot5(1) oDot75(2) lDot0(3)-->

</t\_ReselectionGERAN\_SF>

</CONSTRAINT>

</MDDB>

***Type8：***

***1.*** ***Lte\_SystemInformationBlockType8.xml（主要使用）***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType8" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<systemTimeInfo ACTIVE="1">

<**cdma**\_**EUTRA**\_**Synchronisation** VALUE="**0**"/><!--BOOLEAN-->

<**cdma**\_**SystemTime** SELECT="0"><!--CHOICE **synchronousSystemTime(0)** asynchronousSystemTime(1)-->

<**synchronousSystemTime** VALUE="**000000000000000000000000000000000000000**"/><!--BITSTRING [39]-->

</cdma\_SystemTime>

</systemTimeInfo>

<**parametersHRPD** ACTIVE="**1**">

<preRegistrationInfoHRPD>

<**preRegistrationAllowed** VALUE="**0**"/><!--BOOLEAN-->

</preRegistrationInfoHRPD>

</parametersHRPD>

</CONSTRAINT>

</MDDB>

***Type9：***

***1.*** ***Lte\_SystemInformationBlockType9.xml（主要使用）***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType9" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**hnb**\_**Name** ACTIVE="**1**" VALUE="**00**"/><!--OCTETSTRING [1,48]-->

</CONSTRAINT>

</MDDB>

***Type10：***

***1.*** ***Lte\_SystemInformationBlockType10.xml（主要使用）***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType10" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**messageIdentifier** VALUE="**0001000100000010**"/><!--BITSTRING [16]-->

<**serialNumber** VALUE="**0011000000000000**"/><!--BITSTRING [16]-->

<**warningType** VALUE="**0580**"/><!--OCTETSTRING [2]-->

</CONSTRAINT>

</MDDB>

***Type11：***

***1.*** ***Lte\_SystemInformationBlockType11\_Last.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType11" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**messageIdentifier** VALUE="**0001000100000010**"/><!--BITSTRING [16]-->

<**serialNumber** VALUE="**0011000000000000**"/><!--BITSTRING [16]-->

<**warningMessageSegmentType** VALUE="**1**"/><!--ENUMERATED [0,1] notLastSegment(0) **lastSegment(1)**-->

<**warningMessageSegmentNumber** VALUE="**3**"/><!--INTEGER [0,63]-->

<**warningMessageSegment** VALUE="**993344aa**"/><!--OCTETSTRING [-1]-->

</CONSTRAINT>

</MDDB>

***2.*** ***Lte\_SystemInformationBlockType11\_Seg0.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType11" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**messageIdentifier** VALUE="**0001000100000010**"/><!--BITSTRING [16]-->

<**serialNumber** VALUE="**0011000000000000**"/><!--BITSTRING [16]-->

<**warningMessageSegmentType** VALUE="0"/><!--ENUMERATED [0,1] **notLastSegment(0)** lastSegment(1)-->

<**warningMessageSegmentNumber** VALUE="**0**"/><!--INTEGER [0,63]-->

<**warningMessageSegment** VALUE="**12234355**"/><!--OCTETSTRING [-1]-->

</CONSTRAINT>

</MDDB>

***3.*** ***Lte\_SystemInformationBlockType11\_Seg1.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType11" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**messageIdentifier** VALUE="**0001000100000010**"/><!--BITSTRING [16]-->

<**serialNumber** VALUE="**0011000000000000**"/><!--BITSTRING [16]-->

<**warningMessageSegmentType** VALUE="**0**"/><!--ENUMERATED [0,1] **notLastSegment(0)** lastSegment(1)-->

<**warningMessageSegmentNumber** VALUE="**1**"/><!--INTEGER [0,63]-->

<**warningMessageSegment** VALUE="**66778899**"/><!--OCTETSTRING [-1]-->

</CONSTRAINT>

</MDDB>

***4.*** ***Lte\_SystemInformationBlockType11\_Seg2.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType11" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**messageIdentifier** VALUE="**0001000100000010**"/><!--BITSTRING [16]-->

<**serialNumber** VALUE="**0011000000000000**"/><!--BITSTRING [16]-->

<**warningMessageSegmentType** VALUE="**0**"/><!--ENUMERATED [0,1] **notLastSegment(0)** lastSegment(1)-->

<**warningMessageSegmentNumber** VALUE="**2**"/><!--INTEGER [0,63]-->

<**warningMessageSegment** VALUE="**55111166**"/><!--OCTETSTRING [-1]-->

</CONSTRAINT>

</MDDB>

***5.*** ***Lte\_SystemInformationBlockType11\_Seg3.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType11" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**messageIdentifier** VALUE="**0001000100000010**"/><!--BITSTRING [16]-->

<**serialNumber** VALUE="**0011000000000000**"/><!--BITSTRING [16]-->

<**warningMessageSegmentType** VALUE="1"/><!--ENUMERATED [0,1] notLastSegment(0) **lastSegment(1)**-->

<**warningMessageSegmentNumber** VALUE="**2**"/><!--INTEGER [0,63]-->

<**warningMessageSegment** VALUE="**77222288**"/><!--OCTETSTRING [-1]-->

</CONSTRAINT>

</MDDB>

6. ***Lte\_SystemInformationBlockType11.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType11" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**messageIdentifier** VALUE="**0001000100000010**"/><!--BITSTRING [16]-->

<**serialNumber** VALUE="**0011000000000000**"/><!--BITSTRING [16]-->

<**warningMessageSegmentType** VALUE="0"/><!--ENUMERATED [0,1] **notLastSegment(0)** lastSegment(1)-->

<**warningMessageSegmentNumber** VALUE="**0**"/><!--INTEGER [0,63]-->

<**warningMessageSegment** VALUE="**01**"/><!--OCTETSTRING [-1]-->

</CONSTRAINT>

</MDDB>

***Type12：***

***1.*** ***Lte\_SystemInformationBlockType12\_Seg0.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="Lte\_SystemInformationBlockType12\_r9" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType12\_r9" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**messageIdentifier\_r9** VALUE="**0000000000000001**"/><!--BITSTRING [16]-->

<**serialNumber\_r9** VALUE="**0000000000000000**"/><!--BITSTRING [16]-->

<**warningMessageSegmentType\_r9** VALUE="**0**"/><!--ENUMERATED [0,1] **notLastSegment(0)** lastSegment(1)-->

<**warningMessageSegmentNumber\_r9** VALUE="**0**"/><!--INTEGER [0,63]-->

<**warningMessageSegment**\_r9 VALUE="**1122334455**"/><!--OCTETSTRING [-1]-->

</CONSTRAINT>

</MDDB>

***2.*** ***Lte\_SystemInformationBlockType12\_Seg1.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="Lte\_SystemInformationBlockType12\_r9" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType12\_r9" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**messageIdentifier\_r9** VALUE="**0001000100010010**"/><!--BITSTRING [16]-->

<**serialNumber\_r9** VALUE="**0000000000000001**"/><!--BITSTRING [16]-->

<**warningMessageSegmentType\_r9** VALUE="**1**"/><!--ENUMERATED [0,1] notLastSegment(0) **lastSegment(1)**-->

<**warningMessageSegmentNumber\_r9** VALUE="**1**"/><!--INTEGER [0,63]-->

<**warningMessageSegment\_r9** VALUE="**77889900**"/><!--OCTETSTRING [-1]-->

</CONSTRAINT>

</MDDB>

***2.*** ***Lte\_SystemInformationBlockType12.xml***

<MDDB NAME="LteRrcAsn1.LteRrcAsn1Mddb" USERCOMMENT="Lte\_SystemInformationBlockType12\_r9" MODULE="Lte.Std" USERVERSIONS="28.30.00//7.32.4">

<CONSTRAINT NAME="\_SystemInformationBlockType12\_r9" TYPE="SEND" SIDE="NETWORK" MNEMONIC="">

<**messageIdentifier\_r9** VALUE="**0001000100010010**"/><!--BITSTRING [16]-->

<**serialNumber\_r9** VALUE="**0000000000000001**"/><!--BITSTRING [16]-->

<**warningMessageSegmentType\_r9** VALUE="0"/><!--ENUMERATED [0,1] **notLastSegment(0)** lastSegment(1)-->

<**warningMessageSegmentNumber\_r9** VALUE="**0**"/><!--INTEGER [0,63]-->

<**warningMessageSegment\_r9** VALUE="**0**"/><!--OCTETSTRING [-1]-->

</CONSTRAINT>

</MDDB>